

FIG.1

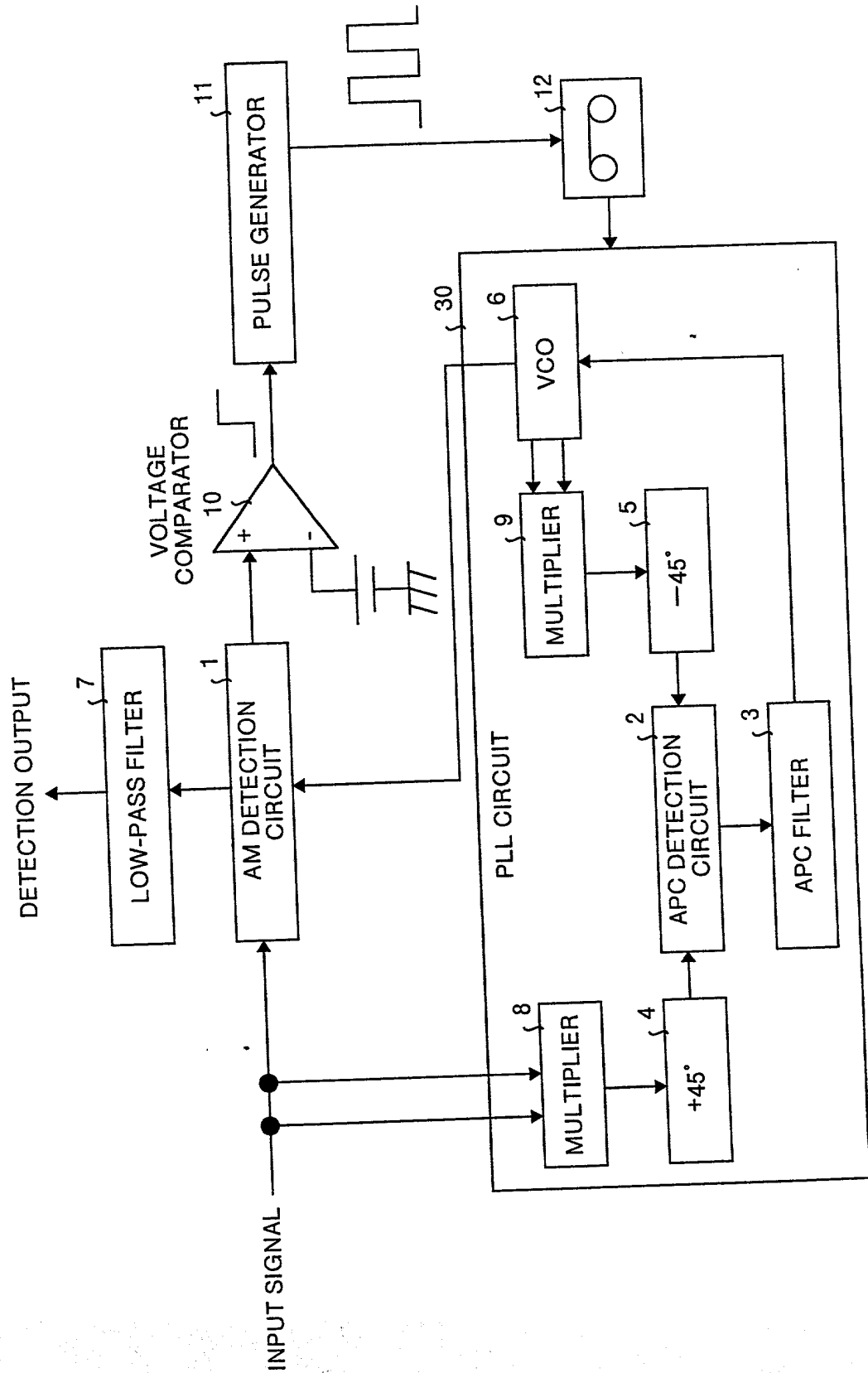


FIG.2A

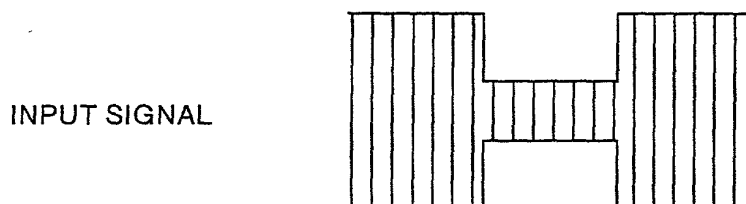


FIG.2B

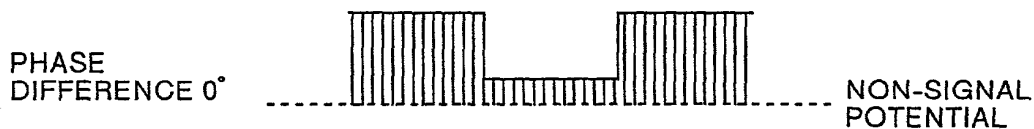


FIG.2C



**FIG. 3**

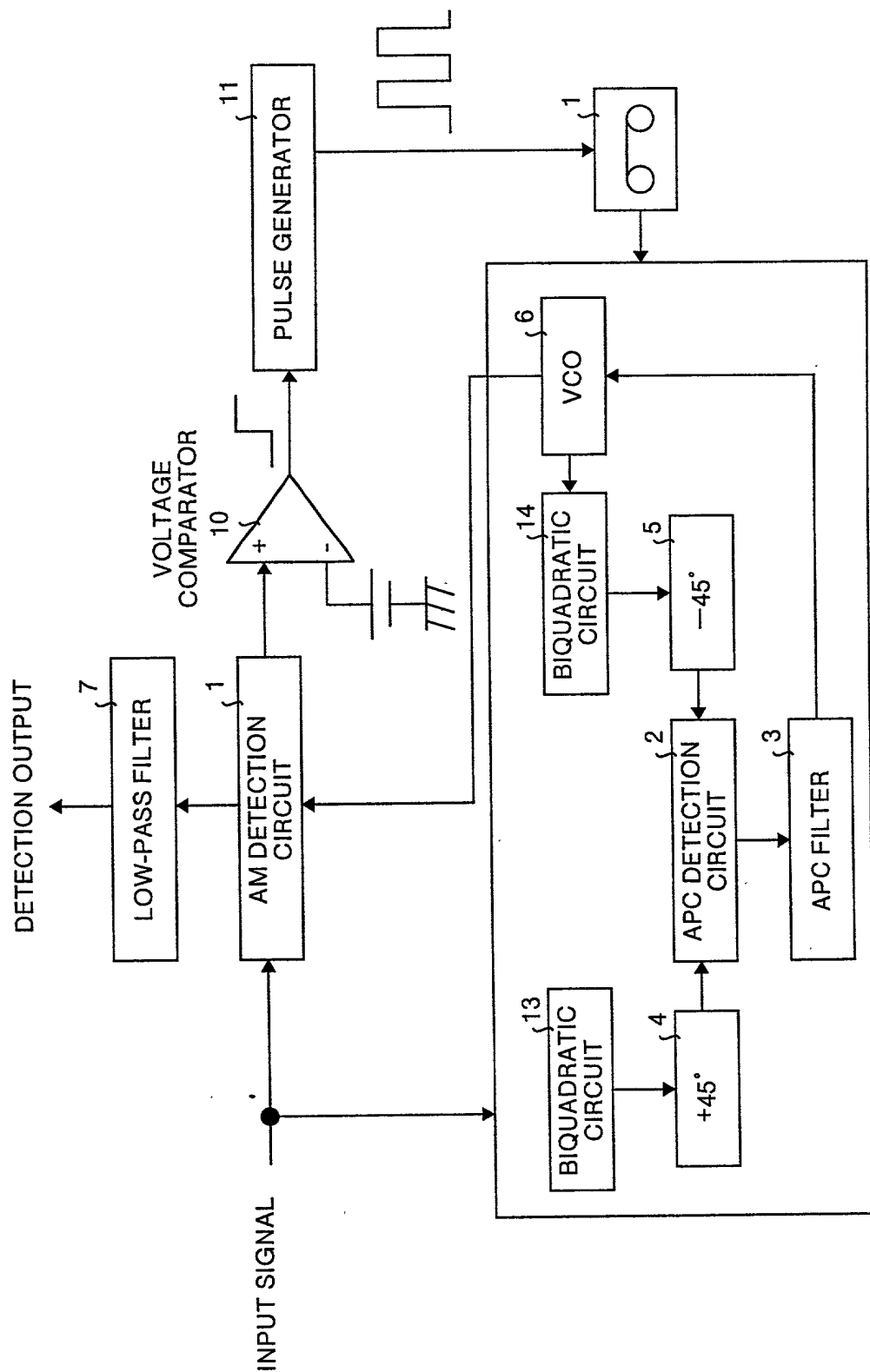


FIG.4A

INPUT SIGNAL

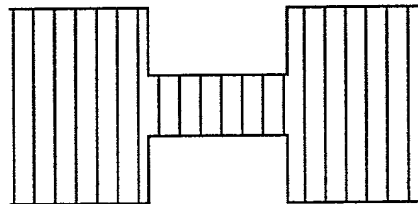


FIG.4B

PHASE  
DIFFERENCE  $0^\circ$

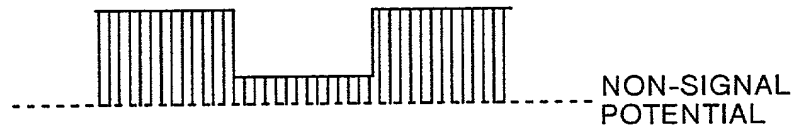


FIG.4C

PHASE  
DIFFERENCE  $90^\circ$

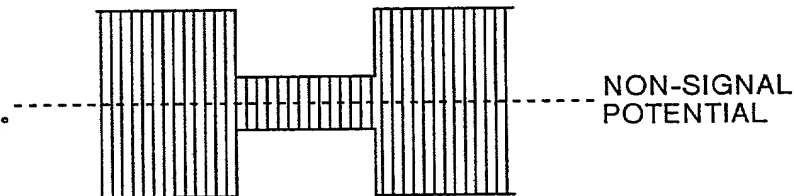


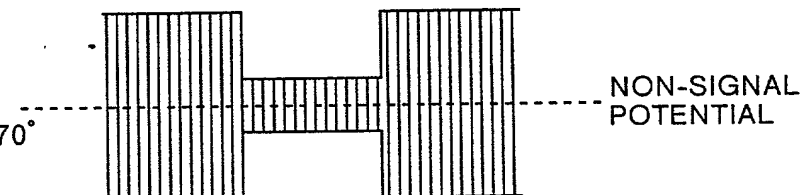
FIG.4D

PHASE  
DIFFERENCE  $180^\circ$



FIG.4E

PHASE  
DIFFERENCE  $270^\circ$



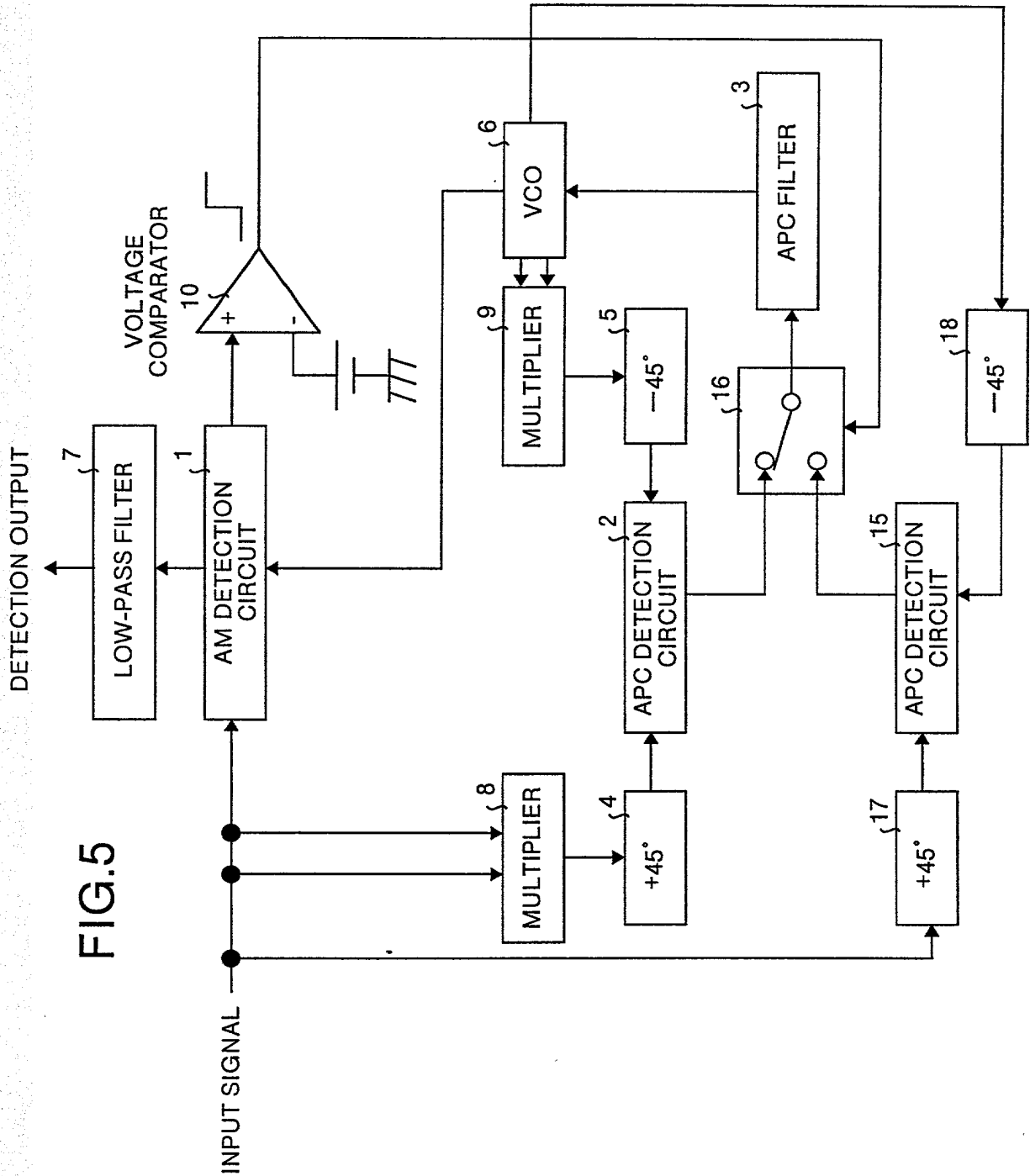
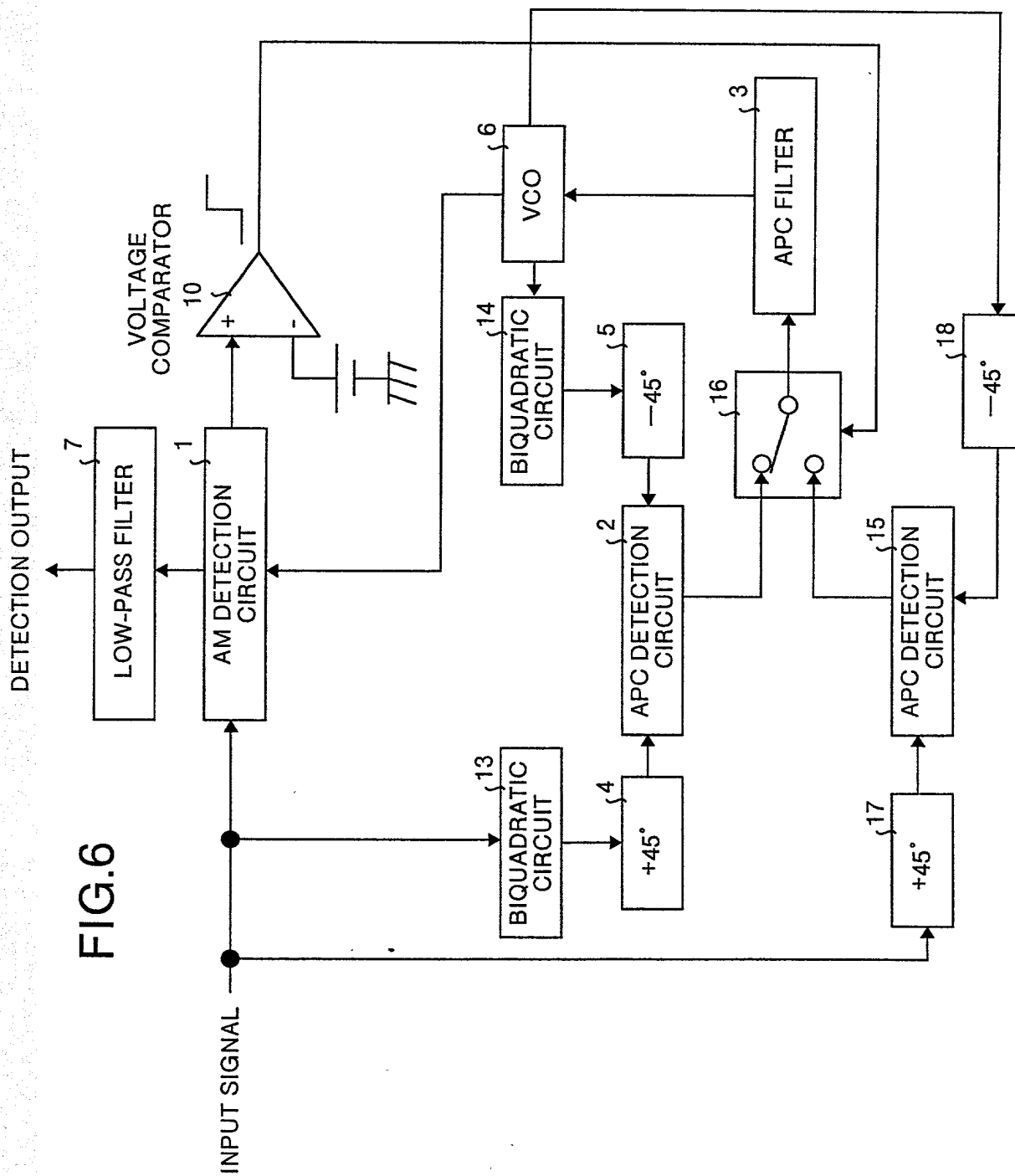


FIG.5

FIG. 5

**FIG. 6**



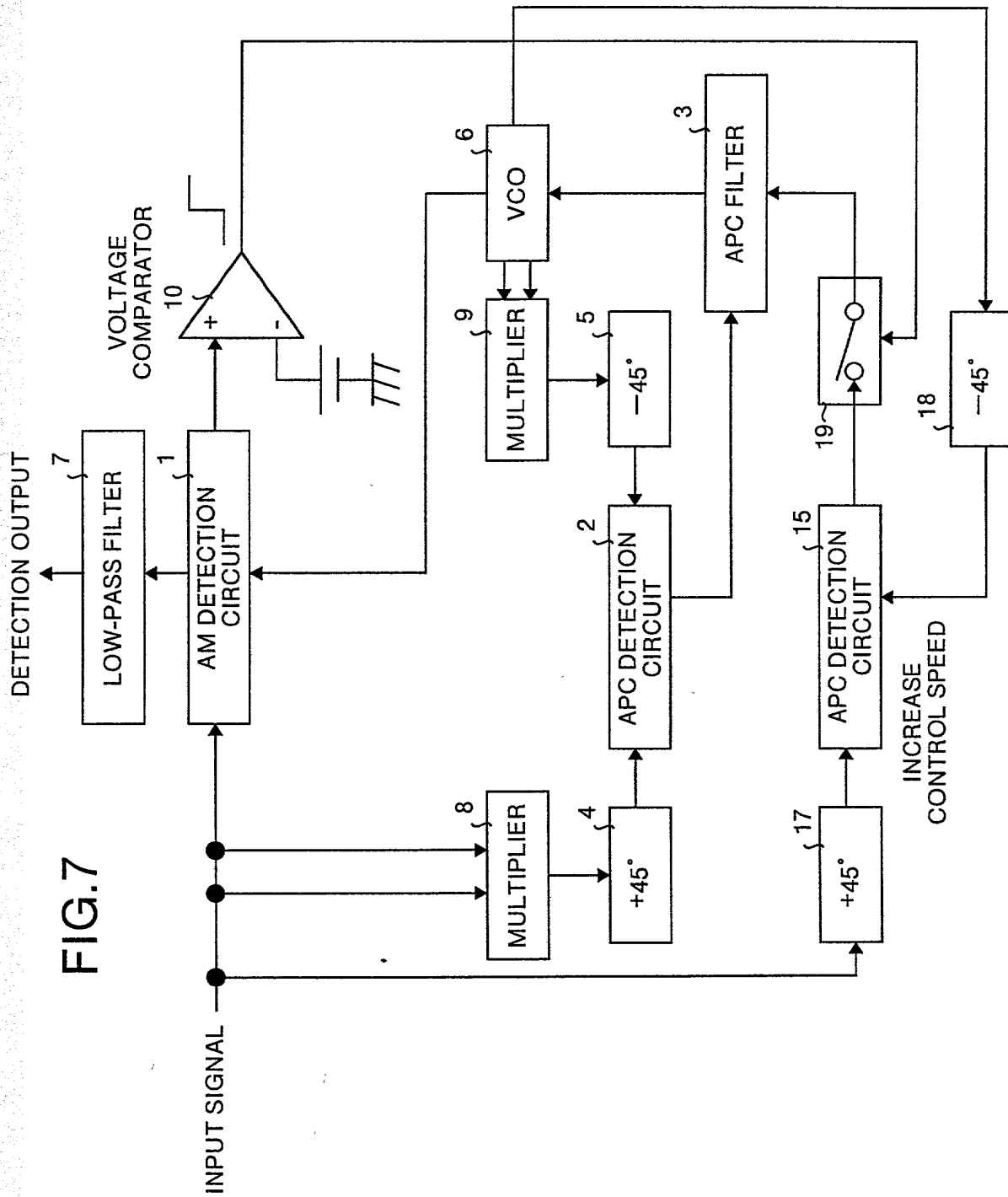


FIG. 7

FIG. 7 225860

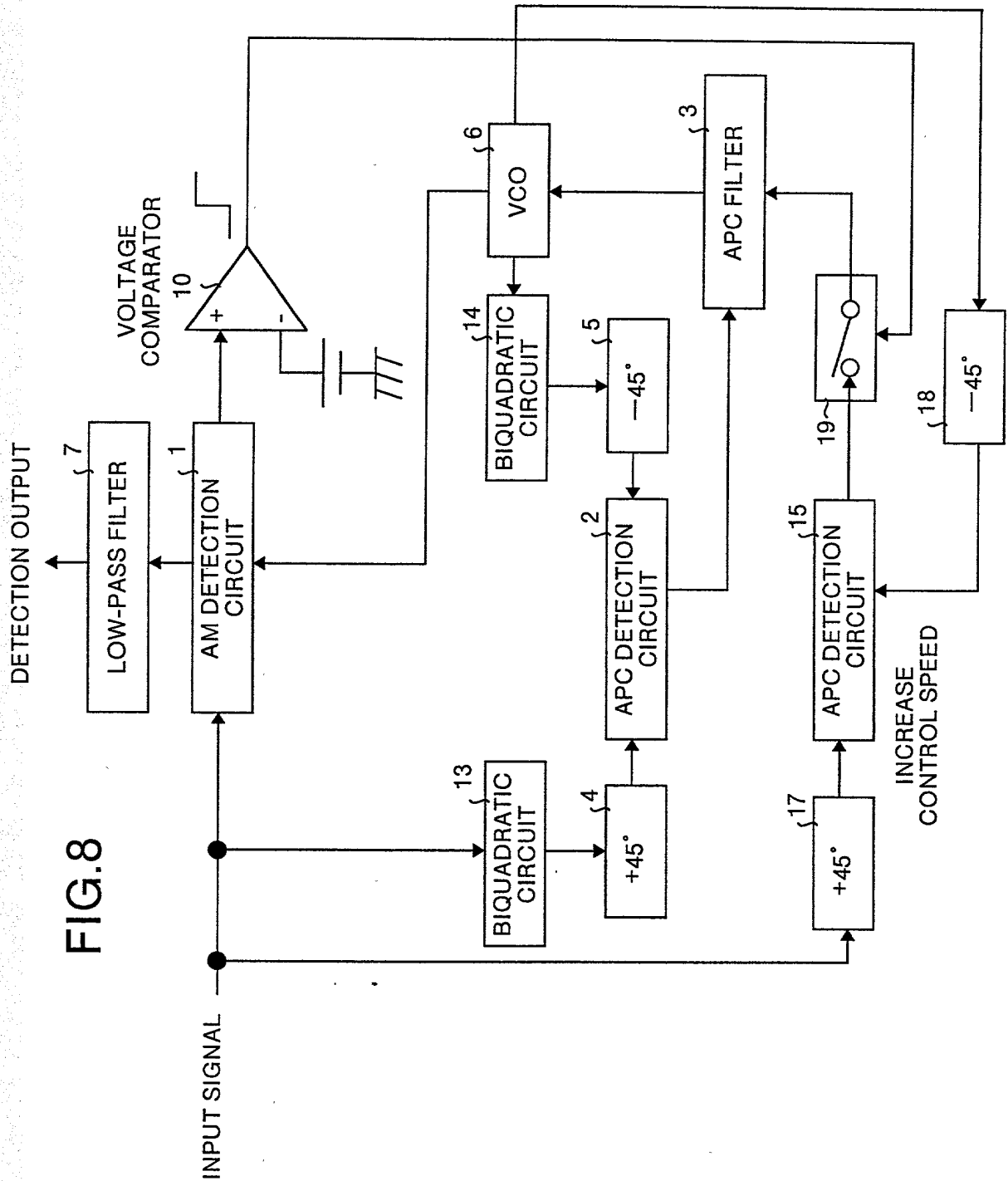


FIG. 8

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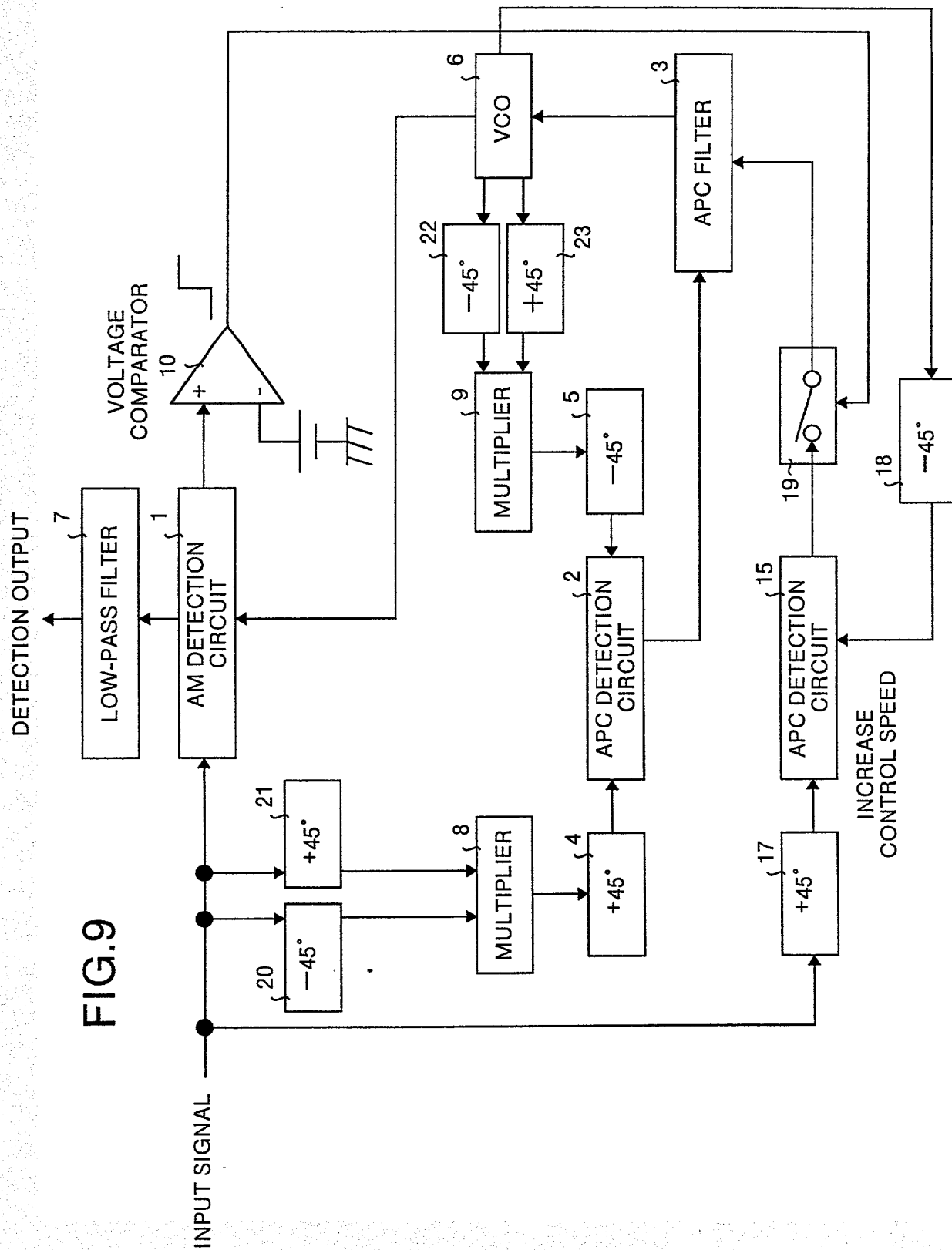


FIG.9

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FIG.10

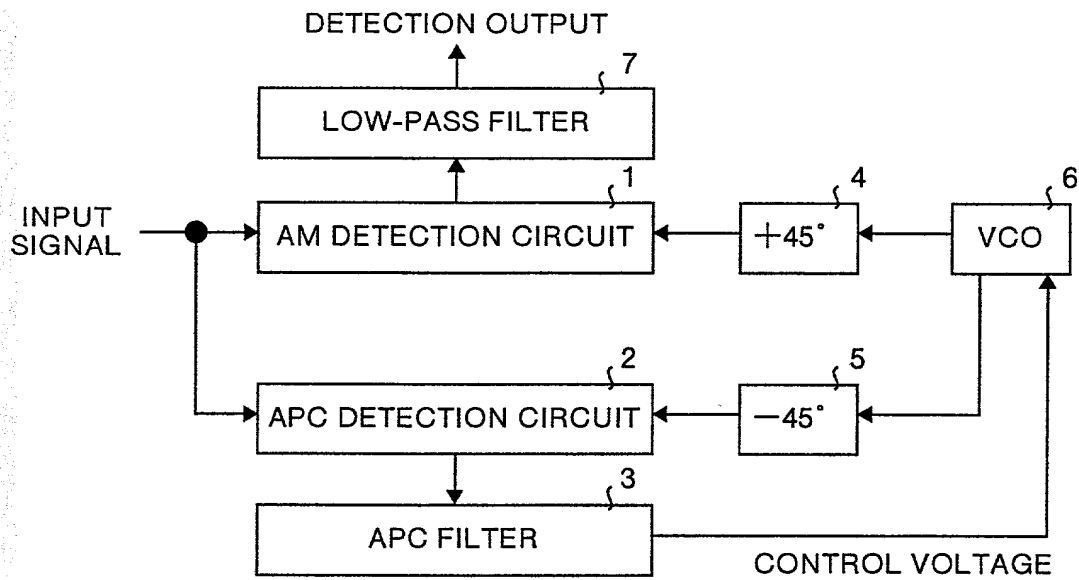


FIG.11A

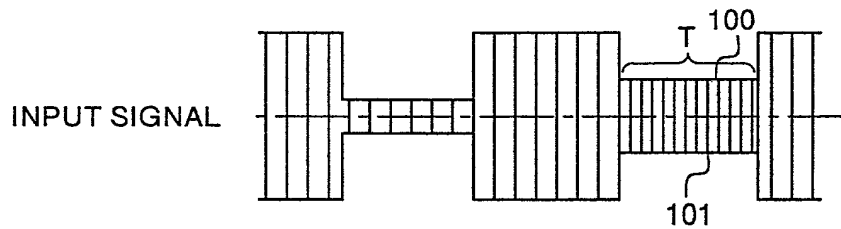


FIG.11B

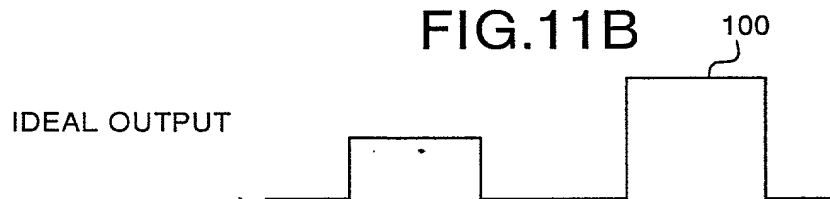


FIG.11C

